

A1  
between the source and the drain in accordance with a prescribed gate bias voltage.

Please replace the paragraph on page 7, starting at line 16 with the following text:

A2  
These and other advantages of the invention will be explained in the description below, in connection with the accompanying drawings, in which:

**IN THE CLAIMS:**

Please replace the text of claims 1-6 with the following text:

1. A charge transfer device having a source follower amplification circuit,

wherein the source follower amplification circuit comprises:

A3  
an amplification transistor which receives, at a gate, a voltage signal from an output section and outputs, from a source, an output signal corresponding to a change in the voltage signal;

a load transistor connected between the amplification transistor and a first power source for causing a constant current to flow from the amplification transistor to the side of the first power source; and

a control transistor connected between the amplification transistor and a second power source, wherein the control transistor controls a current flowing from the second power source to the amplification transistor according to a control signal.

2. The charge transfer device according to claim 1, further comprising an input terminal which is commonly connected to a gate of the control transistor and to a gate of the load transistor.

3. The charge transfer device according to claim 2, further comprising a control signal generating circuit provided between the gate of the control transistor and the input terminal for generating the control signal based on an input signal externally input to the input terminal, and the load transistor maintains an on state with regard to the input signal.

4. The charge transfer device according to claim 1, wherein the control transistor is of an enhancement type.

5. The charge transfer device according to claim 4, further comprising an input terminal which is commonly connected to a gate of the control transistor and to a gate of the load transistor.

6. The charge transfer device according to claim 5, further comprising a control signal generating circuit provided between the gate of the control transistor and the input terminal for generating the control signal based on an input signal externally input to the input terminal, and the load transistor maintains an on state with regard to the input signal.

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Please add the following new claims:

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7. (Newly Added) A charge transfer device having a source follower amplification circuit, wherein said source follower amplification circuit comprises:

means for receiving at a gate, a voltage signal from an output section and outputting, from a source, an output signal corresponding to a change in the voltage signal;

means for causing a constant current to flow from the means for receiving and outputting, to a side of a first power source connected between the means for receiving and outputting and the first power source; and

means for controlling a current flowing from a second power source of the means for receiving and outputting according to a control signal connected between the means for receiving and outputting and the second power source.

8. (Newly Added) The charge transfer device according to claim 7, further comprising an input terminal which is commonly connected to a gate of the means for controlling a current and to a gate of the means for causing a constant current.

9. (Newly Added) The charge transfer device according to claim 8, further comprising means for generating the control signal based on an input signal externally input to the input terminal, and the means for causing a constant current maintains an on state with regard to the input signal, the means for generating the control signal is provided between the gate of the means for controlling a current and the input terminal.

10. (Newly Added) The charge transfer device according to claim 7, wherein the means for controlling a current is of an enhancement type.

11. (Newly Added) The charge transfer device according to claim 10, further comprising an input terminal which is commonly connected to a gate of the means for controlling a current and to a gate of the means for causing a constant current.

12. (Newly Added) The charge transfer device according to claim 11, further comprising means for generating the control signal based on an input signal externally input to the input terminal, and the means for causing a constant current maintains an on state with regard to the input signal, the means for generating the control signal is

provided between the gate of the means for controlling a current and the input terminal.

13. (Newly added) The charge transfer device according to claim 7, further comprising means for reducing a current flowing in the means for receiving and outputting during the period in which a pixel signal is not read out.

14. (Newly added) The charge transfer device according to claim 7, further comprising means for reducing a current flowing in the means for receiving and outputting though imaging is performed.

15. (Newly added) The charge transfer device according to claim 7, further comprising means for increasing or reducing a current flowing in the means for receiving and outputting suitable for each image quality of imaging modes.

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